IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	Confirmation No.: 4829
Yoshinobu Kono	Examiner: Phat X. Cao
Application No.: 10/591,009	Art Unit: 2814
Filed: August 28, 2006	REQUEST TO PLACE REFERENCES INTO THE APPLICATION FILE PER MPEP 609.05(a)
For: INSULATED GATE SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING SAME	
Customer No.: 20350	

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
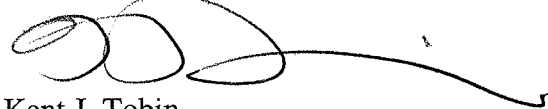Alexandria, VA 22313-1450

Commissioner:

The references cited on attached form PTO/SB/08A and PTO/SB/08B are being

called to the attention of the Examiner. Copies of the references [in compliance with the

requirements of 37 CFR §1.98(a)(2)] are enclosed. As provided for by 37 CFR §1.97(g) and (h),

no inference should be made that the information and references cited are prior art merely

because they are in this statement and no representation is being made that a search has been

conducted or that this statement encompasses all the possible relevant information.

As these references are being submitted after the issuance of the notice of

allowance, pursuant to MPEP 609.05(a), the Examiner is requested to place these references into

the application file .

Applicant believes that <u>no fee is required</u> for submission of this statement.

However, if a fee is required, the Commissioner is authorized to deduct such fee from the

undersigned's Deposit Account No. 20-1430. Please deduct any additional fees from, or credit

any overpayment to, the above-noted Deposit Account.

Respectfully submitted,

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